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## PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Application No.: 09/673,208

Filed: October 13, 2000

SEMICONDUCTOR DEVICE, MOUNTING SUBSTRATE AND METHOD OF For:

MANUFACTURING MOUNTING SUBSTRATE, CIRCUIT BOARD, AND

ELECTRONIC INSTRUMENT

# NOTIFICATION OF ERRORS IN FILING RECEIPT

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Respectfully submitted,

ATTN: Application Branch

Docket No.: 107281

James A. Oliff

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Thomas J. Pardini

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Date: December 12, 2000

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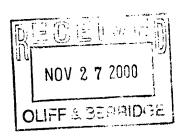
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAVVIIVGS	E 2	1.4
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FILING RECEIPT

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Applicant(s)

Nobuaki Hashimoto, Nagano-ken, JAPAN;

Continuing Data as Claimed by Applicant

THIS APPLICATION IS A 371 OF PCT/JP00/00894 02/17/2000

**Foreign Applications** 

JAPAN 11-039623(P) 02/18/1999

If Required, Foreign Filing License Granted 11/20/2000

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Title

Semiconductor device, circuit board, method of manufacturing circuit board, and electronic device Mounting Substrate and method of manufacturing mounting Substrate, Circuit Board, and electronic instrument.

**Preliminary Class** 

257

Data entry by : ORDENEZ, MARTA

Team: OIPE

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